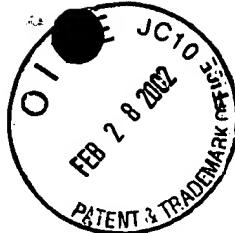


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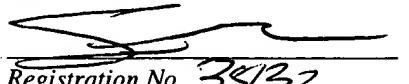


*#111B/Expt of Time
3-19-02
R. Givler*
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re U.S. Patent Application)
Applicant: Nayfeh et al.)
Serial No. 09/496,506)
Filed: February 2, 2000)
For: SILICON NANOPARTICLE)
FIELD EFFECT TRANSISTOR)
AND TRANSISTOR MEMORY)
DEVICE)
Art Unit: 1741)
Examiner: Crane, S.)

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on this date.

2/19/02 
Date 2/19/02 Registration No. 35132
F-CLASS.WCM
Appr. February 20, 1998

Attorney for Applicant

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AMENDMENT A

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In response to the office action mailed October 4, 2001, please amend the application as follows:

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In the Claims:

Please cancel claim 3 and amend claims 1, 5, 6, 7 and 8 as follows: